

11/5/01

Docket No.: 0068-0399-0

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF: :

Masakazu KANECHIKA, et al. : EXAMINER: J. GARCIA

SERIAL NO: 09/420,524 :

FILED: OCTOBER 18, 1999 : GROUP ART UNIT: 2823

FOR: METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE AND
SEMICONDUCTOR DEVICE

TECHNOLOGY CENTER 2800
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RESPONSE TO RESTRICTION REQUIREMENT

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

In response to the Office Action mailed September 04, 2001, Applicants elect, with
traverse, Group II, Claims 10-29, and 32, for further prosecution.

REMARKS

The Office has restricted this application as follows under 35 U.S.C. §121:

Group I: Claims 1-9, 30, and 31, drawn to a method; and

Group II: Claims 10-29, and 32, drawn to a device.

Applicants have elected Group II, Claims 10-29, and 32, with traverse.

The Office has characterized the relationship between Groups I and II as "process of
making and product made." Citing MPEP §806.05(f), the Office suggests that the process as
claimed can be used to make another and a materially different product, such as a
semiconductor device "that does not include a conic body having a radius of curvature of